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## Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claims 1-20 (canceled)

Claim 21 (currently amended): An adapter to read and operate Smart Cards, said system adapter comprising:

a housing adapted to receive a smart eard Smart Card and having a pinout arrangement adapted to interface with a PCMCIA socket; and

a circuit adapted to control selected signal lines of said pinout arrangement to permit a smart eard Smart Card controller to read and operate said smart eard Smart Card through said PCMCIA socket.

Claim 22 (previously presented): An adapter as claimed in claim 21, wherein said pinout arrangement of said second expansion card comprising a PCMCIA Type II/III pinout arrangement.

Claim 23 (currently amended): An adapter to read and operate Smart Cards, said system comprising:

an integrated controller comprising a first circuit for detecting said Smart Card, a second circuit enabled by said first circuit for operating said Smart Card, and a third circuit enabled by

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said first and second circuits to provide communication between said Smart Card and a Smart Card controller and between said Smart Card and a bus controller logic using conventional PC card communication protocols; and

an adapted adapter enabled to provide operability of Smart Card in a PCMCIA type II/III socket.

Claim 24 (previously presented): A system as claimed in claim 23, wherein said passive adapter comprising a circuit and a connector, said circuit connected to said connector and adapted to operate said first expansion card and to generate signals operable through said socket.

Claim 25 (previously presented): A system as claimed in claim 23, wherein said first expansion card type comprising a Smart Card.

Claim 26 (previously presented): A system as claimed in claim 23, wherein said first expansion card type comprising a flash memory card.

Claim 27 (previously presented): A system as claimed in claim 23, wherein said socket having a pinout arrangement comprising a PCMCIA Type II/III pinout arrangement.